

Patent Abstracts of Japan

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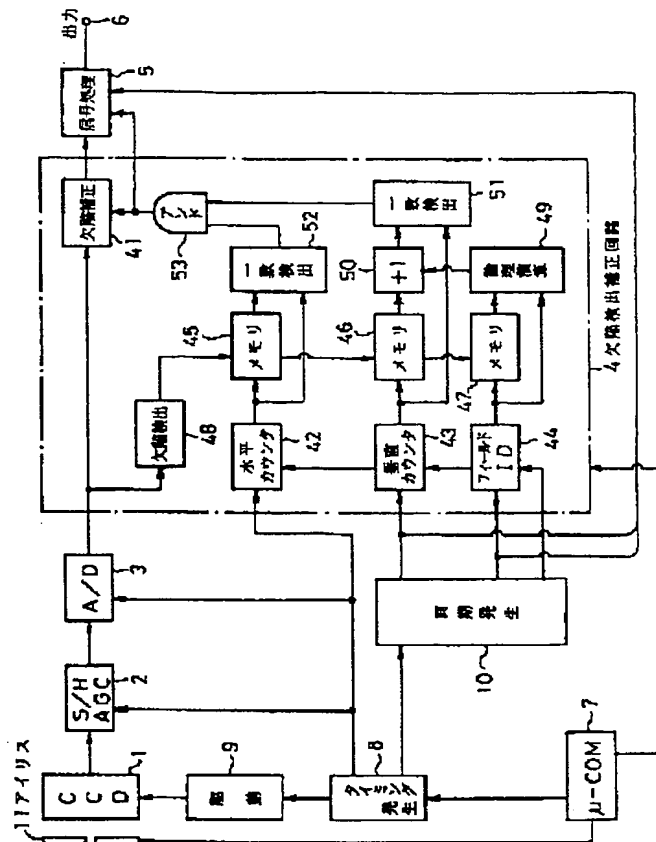
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APPLICANT : SONY CORP;

INVENTOR : SHIMURA MASAYUKI;

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TITLE : DEFECT CORRECTION METHOD AND
DEVICE FOR SOLID STATE IMAGE
PICKUP DEVICE



ABSTRACT : PROBLEM TO BE SOLVED: To reduce the capacity of a storage which stores the locations of defects.

SOLUTION: A horizontal address, a vertical address and a field ID (identification) signal are generated at a horizontal counter 42, a vertical counter 43 and a field ID circuit 44 and then supplied to the memories 45, 46 and 47 respectively. The digital video signals received from an A/D converter 3 are supplied to a defect detection circuit 48, and the defect detection signals are supplied to the memories 45 to 47 which store the horizontal and vertical addresses and the field ID signal respectively. The field ID signal sent from the circuit 44 and the identification signal stored in the memory 47 are supplied to a logical operation circuit 49. The logical output signal of the circuit 49 is supplied to a '1' addition circuit 50, and '1' is added to the vertical address stored in the memory 46. Then the vertical address including added '1' and the horizontal address are supplied to the coincidence detection circuits 51 and 52 respectively. When the coincidence of both addresses are confirmed at the circuits 51 and 52, the detection output is supplied to a defect correction circuit 41.

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